

Cinematronics EPROM Board 1.0 Patch Options

Options below require capacitors installed in C3, C4, C5, C6

2716 CCPU -> 2716

Roms: P7, T7  
Bank: 1

Tested

1	ok: not used	+5
1		GND
2	ok: not used	+5
2	ok: not used	GND
2		P7-18
21		P7-18
21		P7-21
27	ok: not used	+5
27		GND
27		A14 (NC)
26 (24)		+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)		U7-18
CE (PT)		P7-18
CE (PT)		GND

2716 CCPU -> 2716

Roms: P7, R7, T7, U7  
Banks: 1,2  
(Selected by CE)

Tested

1	ok: not used	+5
1		GND
2	ok: not used	+5
2	ok: not used	GND
2		P7-18
21		P7-18
21		P7-21
27	ok: not used	+5
27		GND
27		A14 (NC)
26 (24)		+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)		U7-18
CE (PT)		P7-18
CE (PT)		GND

PROM CCPU -> 2732 (PT)

Roms: P7, T7

Banks: 1,2  
(Selected by CE)

Tested

1	ok: not used	+5
1		GND
2	ok: not used	GND
2	ok: not used	P7-18
21		P7-18
21		P7-21
27	ok: not used	+5
27		GND
27		A14 (NC)
26 (24)		+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)		U7-18
CE (PT)		P7-18
CE (PT)		GND

2732 CCPU -> 2732

Roms: P7, T7  
Banks: 0,1

Tested

1	ok: not used	+5
1		GND
2	ok: not used	+5
2	ok: not used	GND
2		P7-18
21		P7-18
21		P7-21
27	ok: not used	+5
27		GND
27		A14 (NC)
26 (24)		+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)		U7-18
CE (PT)	ok: not used	P7-18
CE (PT)		GND

2732 CCPU -> 2732

Roms: P7, R7, T7, U7  
Banks: 0,1,2,3  
(Selected by CE)

Tested

1	ok: not used	+5
1		GND
2	ok: not used	+5
2	ok: not used	GND
2		P7-18
21		P7-18
21		P7-21
27	ok: not used	+5
27		GND
27		A14 (NC)
26 (24)		+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)		U7-18
CE (PT)		P7-18
CE (PT)		GND

2732 CCPU -> 2732 or 2764

Roms: P7, T7  
2732 Banks: [0,1]  
2764 Banks: [0,1] [0,1]  
(Selected by Jumper)

Tested

1	2764 needs	+5
1		GND
2	High or Low	+5
2	High or Low	GND
2		P7-18
21		P7-18
21		P7-21
27	2764 needs	+5
27		GND
27		A14 (NC)
26 (24)	2732 needs	+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)	ok: not used	U7-18
CE (PT)		P7-18
CE (PT)		GND

2732 CCPU -> 2732 or 2764

Roms: P7, R7, T7, U7  
2732 Banks: [0,1,2,3]  
2764 Banks: [0,1,2,3] [0,1,2,3]  
(Selected by Jumper & CE)

Tested

1	2764 needs	+5
1		GND
2	High or Low	+5
2	High or Low	GND
2		P7-18
21		P7-18
21		P7-21
27	2764 needs	+5
27		GND
27		A14 (NC)
26 (24)	2732 needs	+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)		U7-18
CE (PT)		P7-18
CE (PT)		GND

2732 CCPU -> 2764 (TP)

Roms: P7, T7  
2764 Banks: [0,1,2,3]  
[P+R,T+U]  
(Selected by CE -> A12 pin-2)

Tested

1		+5
1		GND
2		+5
2		GND
2		P7-18
21		P7-18
21		P7-21
27		+5
27		GND
27		A14 (NC)
26 (24)	ok: not used	+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)	ok: not used	U7-18
CE (PT)		P7-18
CE (PT)		GND

PT Game 1  
RU

Boxing Bugs -> 2764

Roms: P7, R7, T7, U7  
2764 Banks: [0,1,2,3,4,5,6,7]  
(Selected by CE & U4)

Tested

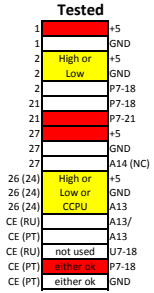
1		+5
1		GND
2		+5
2		GND
2		P7-18
21		P7-18
21		P7-21
27		+5
27		GND
27		A14 (NC)
26 (24)		+5
26 (24)		GND
26 (24)		A13
CE (RU)		A13/
CE (PT)		A13
CE (RU)		U7-18
CE (PT)		P7-18
CE (PT)		GND

Options below require capacitors installed in C7, C8, C9, C10

**2732 CCPU -> 27128**

Roms: P7, T7  
Banks: [0,1] \* 4

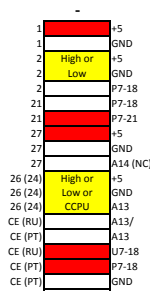
Selected by Jumper



**2732 CCPU -> 27128**

Roms: P7, R7, T7, U7  
Banks: [0,1,2,3] \* 4

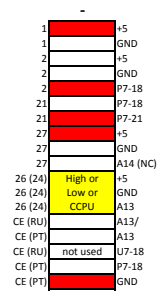
Selected by Jumper & CE



**2732 CCPU -> 27128\***

Roms: P7, T7  
Banks: [0,1,2,3] \* 2  
[P+R,T+U] \* 2

Selected by Jumper & CE -> A12 pin-2

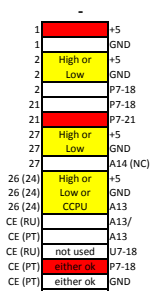


PT Game 1  
RU  
PT Game 2  
RU

**2732 CCPU -> 27256**

Roms: P7, T7  
Banks: [0,1] \* 8

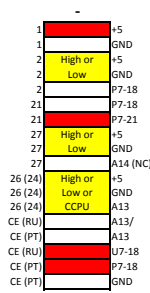
Selected by Jumper



**2732 CCPU -> 27256**

Roms: P7, R7, T7, U7  
Banks: [0,1,2,3] \* 8

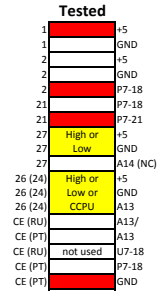
Selected by Jumper & CE



**2732 CCPU -> 27256\***

Roms: P7, T7  
Banks: [0,1,2,3] \* 4  
[P+R,T+U] \* 4

Selected by Jumper & CE -> A12 pin-2

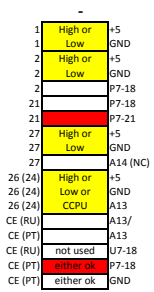


PT Game 1  
RU  
PT Game 2  
RU  
PT Game 3  
RU  
PT Game 4  
RU

**2732 CCPU -> 27512**

Roms: P7, T7  
Banks: [0,1] \* 16

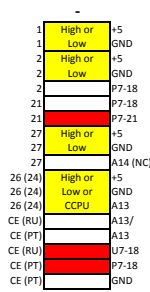
Selected by Jumper



**2732 CCPU -> 27512**

Roms: P7, R7, T7, U7  
Banks: [0,1,2,3] \* 16

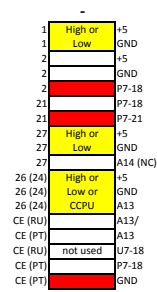
Selected by Jumper & CE



**2732 CCPU -> 27512\***

Roms: P7, T7  
Banks: [0,1,2,3] \* 8  
[P+R,T+U] \* 8

Selected by Jumper & CE -> A12 pin-2



PT Game 1  
RU  
PT Game 2  
RU  
PT Game 3  
RU  
PT Game 4  
RU  
PT Game 5  
RU  
PT Game 6  
RU  
PT Game 7  
RU  
PT Game 8  
RU